



VIDYAVARDHAKA COLLEGE OF ENGINEERING

Gokulam III stage, Mysuru-570 002

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Accredited by NBA New Delhi (Validity: 01.07.2017 – 30.06.2020)

TIME TABLE FOR THE ACADEMIC YEAR 2018 - 2019 (ODD SEMESTER)

SEM: III

Version 1.2

Room No: C 105

Day/Time	7.30 – 8.30	8.30 – 9.30	9.30 – 10.30	10.30 - 11.00	11 – 12	12 – 1	1 - 2	2 - 3	3 - 4	4 - 5
MON		17EE33	17EE36	T e a k B r e a k	17MAT31	17EE34	L u n c h B r e a k			
TUE		17EE32	17EE34		17EE35	17MAT31				
WED		17EE34	17EE33		17EE36	17EE32		17EEL37(B2)(KGR + MBR) / 17EEL38(B1)(AR + RVP), 17MATDIP31(B3)		
THUR		17EE35	17MAT31		17EE32	17EE36 (T/R)		17EEL37(B3)(KGR+MBR) / 17EEL38(B2)(AR + RVP)		
FRI		17EE36	17EE35		17EE34	17EE33		17CPH39	17MAT31 (T/R)	
SAT		17MAT31	17EE32		17EE33	17EE35				

17MAT31-Engineering Mathematics-III(RT)

17EE32 - Electric Circuit Analysis(Dr.RS)

17EE33 - Transformers and Generators(MBR)

17EE34 - Analog Electronic Circuits(GN)

17EE35 - Digital System Design(RVP)

17EE36-Electrical and Electronic Measurements(MJ)

17EEL37 - Electrical Machines Laboratory-1(KGR + MBR)

17EEL38 - Electronics Laboratory(AR+RVP)

17MATDIP31-Additional Mathematics-I

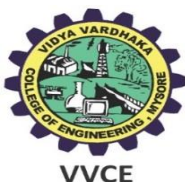
17CPH39-Constitution of India,Professional Ethics and Human Rights(SS)

B R PRASAD & RESHMA V P
(Time Table Co-ordinators)

(Dr. SHOBHA SHANKAR)
Head of the Department

(Dr. D J RAVI)
Dean (Academics)

(Dr. B SADASHIVE GOWDA)
Principal



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TIME TABLE FOR THE ACADEMIC YEAR 2018 - 2019 (ODD SEMESTER)

SEM: V

Version 1.2

Room No: C 104

Day/Time	7.30 – 8.30	8.30 – 9.30	9.30 – 10.30	10.30 - 11.00	11 – 12	12 – 1	1 - 2	2 - 5	
MON		15EE562/ 15EE563	15EE54	T e a B r e a k	15EE553	15EE53	L u n c h B r e a k	15EEL57(B1) (LC + RP+SGK) / 15EEL58(B3)(GN+ HKP)/ B2- CL Educate	
TUE		15EE51	15EE553		15EE52	15EE54			
WED		15EE52	15EE562/ 15EE563		15EE53	15EE51			
THUR		15EE53	15EE52		15EE562/ 15EE563	15EE54 (T/R)			15EEL57(B2) (LC + RP+SGK) / 15EEL58(B1)(GN + HKP)/ B3-CL Educate
FRI		15EE54	15EE51		15EE53	15EE553			15EEL57(B3) (LC + RP+SGK) / 15EEL58(B2)(GN+ HKP)/ B1-CL Educate
SAT		15EE553	15EE54		15EE51	15EE52			

Time Table Coordinators : D BABU RAJENDRA PRASAD & RESHMA V P

15EE51 - Management and Entrepreneurship(BRP)

15EE52 - Microcontroller(RP)

15EE53 - Power Electronics(SGK)

15EE54- Signals and Systems(LC)

15EE553-Estimating and Costing(KGR)

15EE562-Programmable Logic Controllers(MJR)

15EE563-Renewable Energy Systems(HKP)

15EEL57-Microcontroller Laboratory(LC+RP)

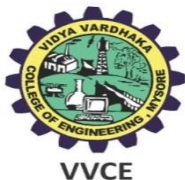
15EEL58-Power Electronics Laboratory(GN+HKP)

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TIME TABLE FOR THE ACADEMIC YEAR 2018 - 2019 (ODD SEMESTER)

SEM: VII

Version 1.1

Room No: C 103

Day/Time	7.30 – 8.30	8.30 – 9.30	9.30 – 10.30	10.30 - 11.00	11 – 12	12 – 1	1 - 2	2 - 5
MON		15EE71	15EE752	T e a B r e a k	15EE72	15EE742	L u n c h B r e a k	15EEL76(B2) (Dr.SS+MJ+BRP) / 15EEL77(B1)(Dr.RS + MJR)
TUE		15EE72	15EE742		15EE73	15EE71		15EEL76(B1) (Dr.SS+MJ+BRP) / 15EEL77(B3)(Dr.RS + MJR)
WED		15EE742	15EE73		15EE752	15EE72		
THUR		15EE73	15EE72		15EE71	15EE752		
FRI		15EE752	15EE71		15EE742	15EE73		15EEL76(B3) (Dr.SS+MJ+BRP) / 15EEL77(B2)(Dr.RS + MJR)
SAT		15EEP78			TAP activities			

Time Table Coordinators :D BABU RAJENDRA PRASAD & RESHMA V P

15EE71 - Power System Analysis-II(Dr.SS)

15EE72 - Power System Protection(MBR)

15EE73- High Voltage Engineering(Dr.RS)

15EE742 - Utilization of Electrical Power(MJ)

15EE752-Testing and Commissioning of Power System Apparatus(KGR)

15EEL76-Power System Simulation Laboratory(Dr.SS+MJ+BRP)

15EEL77-Relay and High Voltage Laboratory(Dr.RS+MJR)

15EEP78-Project Phase-1+Seminar

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